jc525 U.S. PTO
09/542024
04/03/00

Class
Subclass
ISSUE CLASSIFICATION

PATENT NUMBER

U.S. **UTILITY** Patent Application

	O.I.P.E.	PATENT DATE
12W SCANNED TrO Q.A. Me		

APPLICATION NO. 09/542024	CONT/PRIOR	CLASS 712 703	SUBCLASS 8	ART UNIT 2782 2703	EXAMINER BRODA

APPLICANTS
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TITLE
Methods and arrangements for automatically interconnecting cores in systems-on-chip

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)				
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)			
INTERNATIONAL CLASSIFICATION						
						☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner)		(Date)	NOTICE OF ALLOWANCE MAILED	
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
	(Primary Examiner)		(Date)	Amount Due	Date Paid
☐ The terminal ___ months of this patent have been disclaimed.	(Legal Instruments Examiner)		(Date)	ISSUE BATCH NUMBER	

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